



■ **UltraDrive GX Series**

**SATA Solid State Drive**

**SATA Flash Drive Pin Assignment**

**Power Segment**

PIN	Signal Name	Description
P1	Not Used (3.3V)	N/A
P2	Not Used (3.3V)	N/A
P3	Not Used (3.3V Precharge)	
P4	GND	1 <sup>st</sup> mate
P5	GND	2 <sup>nd</sup> mate
P6	GND	
P7	5V Precharge	5V Power
P8	5V Precharge	5V Power
P9	5V Precharge	
P10	GND	
P11	Reserved	
P12	GND	1 <sup>st</sup> mate
P13	Not Used (12V Precharge)	N/A
P14	Not Used (12V)	
P15	Not Used (12V)	

**Signal Segment**

PIN	Signal Name	Description
S1	GND	
S2	RxP	Differential Signal pair for Receive
S3	RxN	
S4	GND	
S5	TxN	Differential Signal pair for Transmit
S6	TxP	
S7	GND	

**Revision History**

December 5, 2008 Rev-A Preliminary Product Specification Released  
 May 7, 2009 Rev-B Updated Performance Specifications  
 November 3, 2009 Rev-C Combined MLC and SLC models into one data sheet

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**Mechanical Specifications**

